

IN THE CLAIMS

1. (cancel)

2. (cancel)

3. (cancel)

4. (cancel)

5. (previously presented) The microelectronic assembly as claimed in claim 9, wherein said first microelectronic element is selected from the group consisting of a semiconductor chip, a semiconductor wafer, a semiconductor chip package, a circuit board, a dielectric sheet, a circuit panel, a connection component, an interposer, a substrate and a dielectric substrate.

6. (previously presented) The microelectronic assembly as claimed in claim 9, wherein said second microelectronic element is selected from the group consisting of a semiconductor chip, a semiconductor wafer, a semiconductor chip package, a circuit board, a dielectric sheet, a circuit panel, a connection component, an interposer, a substrate and a dielectric substrate.

7. (cancel)

8. (previously presented) The microelectronic assembly as claimed in claim 9, wherein said at least one lead comprises an array of flexible leads extending between and electrically interconnecting said first and second microelectronic elements.

9. (currently amended) A microelectronic assembly comprising:

a first microelectronic element having a contact bearing face and at least one contact accessible at the contact bearing face;

a second microelectronic element opposing said first microelectronic element, said second microelectronic element

having a first surface including at least one lead extending over the first surface;

a first fusible material engaging the at least one contact of said first microelectronic element;

a second fusible material engaging the at least one lead, wherein one of said first and second fusible materials has a higher melting temperature and one of said first and second fusible materials has a lower melting temperature;

said first and second microelectronic elements being juxtaposed with one another so that said first and second fusible materials are in substantial alignment with one another, wherein one of said first and second fusible materials is in a liquid state and one of said first and second fusible materials is in a solid state, wherein said first microelectronic element includes a semiconductor wafer comprising a plurality of semiconductor chips, said wafer being severable for providing individual packages comprising one or more of said semiconductor chips electrically interconnected with at least a region of said second microelectronic element.

10. (previously presented) The microelectronic assembly as claimed in claim 9, wherein said first and second fusible materials are spaced from one another.

11. (previously presented) The microelectronic assembly as claimed in claim 9, wherein said first and second fusible materials are conductive.

12. (previously presented) The microelectronic assembly as claimed in claim 9, wherein said first and second fusible materials are connectable together for electrically interconnecting said first and second microelectronic elements.

13. (previously presented) The microelectronic assembly as claimed in claim 9, wherein the at least one lead has a first end permanently attached to said second

microelectronic element and a second end releasably attached to said second microelectronic element.

14. (previously presented) The microelectronic assembly as claimed in claim 9, wherein the at least one lead overlies the first surface of said second microelectronic element.

15. (previously presented) The microelectronic assembly as claimed in claim 9, wherein the at least one lead is flexible.

16. (cancel)

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21. (cancel)

22. (cancel)

23. (previously presented) A microelectronic assembly comprising:

a first microelectronic element having a contact bearing face and one or more contacts provided at the contact bearing face;

a second microelectronic element juxtaposed with said first microelectronic element, said second microelectronic element having a first surface including one or more conductive pads;

one or more conductive masses electrically interconnecting the contacts of said first microelectronic element and the conductive pads of said second microelectronic element, wherein each said conductive mass includes a first region comprising a first fusible material transformable from a solid to a liquid at a first melting temperature and a second region comprising a second fusible material transformable from a

solid to a liquid at a second melting temperature that is less than the first melting temperature; and

at least one flexible lead extending between and electrically interconnecting said first and second microelectronic elements, wherein said first microelectronic element includes a semiconductor wafer comprising a plurality of semiconductor chips, said wafer being severable for providing individual packages comprising one or more of said semiconductor chips electrically interconnected with at least a region of said second microelectronic element.